

PTO/SB/08A (08-03)

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Complete if Known

Application Number	09/737,226
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Filing Date	12/14/2000
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First Named Inventor	Trezza, John A. et al
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Art Unit	2633
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Examiner Name	Tran, Dzung D.
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Attorney Docket Number	LSC141J
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Sheet 1

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Attorney Docket Number

LSC141J

U. S. PATENT DOCUMENTS

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FOREIGN PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No.	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T ^o
		Country Code ² *Number ⁴ *Kind Code ⁶ (if known)				
TD		JP 03 089327	04-15-1991	Hikari Gijutsu Kenkyu		

Examiner
Signature

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Date

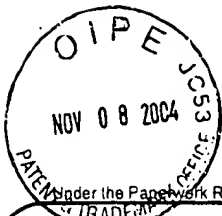
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04/28/2005

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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Application Number	09/737,226
Filing Date	12/14/2000
First Named Inventor	Trezza, John A. et al.
Art Unit	2633
Examiner Name	Tran, Dzong D.
Attorney Docket Number	LSC141J

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Sheet 2

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NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
TD		Zhao, Chunhe et al. "General Purpose Bidirectional Optical Backplane: High-performance Bus for Multiprocessor Systems", IEEE, July 1995, pp. 188-194.	
TD		European Search Report dated October 7, 2004 of Application No. EP 00 99 3221 International filing date December 20, 2000.	

Examiner Signature	<i>Dzung Tran</i>	Date Considered	04/18/2005
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